

<u>L27</u>	L26 and USB	0	<u>L27</u>
<u>L26</u>	L24 and controller	2	<u>L26</u>
<u>L25</u>	L24 and control adj unit	1	<u>L25</u>
<u>L24</u>	L23 and interface	4	<u>L24</u>
<u>L23</u>	active adj state and passive adj state and imaging	13	<u>L23</u>
<u>L22</u>	imaging and active and passive	4029	<u>L22</u>
<u>L21</u>	L20 and active and passive	0	<u>L21</u>
<u>L20</u>	RS232C and RS422 and USB	4	<u>L20</u>
<u>L19</u>	L11 and imaging	6	<u>L19</u>
<u>L18</u>	L11 and imaging and video adj camera	0	<u>L18</u>
<u>L17</u>	L14 and communication adj standard	1	<u>L17</u>
<u>L16</u>	L13 and communication adj standard	5	<u>L16</u>
<u>L15</u>	L13 and standard	55	<u>L15</u>
<u>L14</u>	L12 and control adj unit	35	<u>L14</u>
<u>L13</u>	L12 and controller	76	<u>L13</u>
<u>L12</u>	L11	133	<u>L12</u>
<u>L11</u>	active and passive and first adj interface and second adj interface	133	<u>L11</u>
<u>L10</u>	L7 and interface	1	<u>L10</u>
<u>L9</u>	L8 and interface	5	<u>L9</u>
<u>L8</u>	L6 and controller	5	<u>L8</u>
<u>L7</u>	L6 and control adj unit	1	<u>L7</u>
<u>L6</u>	L5 and second adj communication adj interface	7	<u>L6</u>
<u>L5</u>	passive and active and first adj communication adj interface	8	<u>L5</u>
<u>L4</u>	L3 and control adj unit	1	<u>L4</u>
<u>L3</u>	L1 and first adj interface and second adj interface	1	<u>L3</u>
<u>L2</u>	L1 and first adj communication adj interface	0	<u>L2</u>
<u>L1</u>	active adj state and passive adj state	264	<u>L1</u>

END OF SEARCH HISTORY

side by side

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L62</u>	IEEE adj 1394 and camera and interface and inactive and passive	5	<u>L62</u>
<u>L61</u>	IEEE adj 1394 and camera and interface and active and passive	37	<u>L61</u>
<u>L60</u>	L59 and camera	1	<u>L60</u>
<u>L59</u>	L56 and IEEE adj 1394	1	<u>L59</u>
<u>L58</u>	L57 and IEEE adj 1394	0	<u>L58</u>
<u>L57</u>	L56 and imaging	6	<u>L57</u>
<u>L56</u>	first adj interface and second adj interface and active and passive	133	<u>L56</u>
<u>L55</u>	first adj interface and second adj interface and imaging and IEEE adj 1394	1	<u>L55</u>
<u>L54</u>	first adj interface and second adj interface and imaging and IEEE1394	0	<u>L54</u>
<u>L53</u>	active and passive and imaging and IEEE adj 1394	13	<u>L53</u>
<u>L52</u>	active and passive and imaging and IEEE1394	2	<u>L52</u>
<u>L51</u>	active and passive and imaging and 1394	52	<u>L51</u>
<u>L50</u>	L48 and USB	0	<u>L50</u>
<u>L49</u>	L47 and RS422	0	<u>L49</u>
<u>L48</u>	L47 and RS adj 422	0	<u>L48</u>
<u>L47</u>	1394 and active adj state and passive adj state	2	<u>L47</u>
<u>L46</u>	IEEE adj 1394 and active adj state and passive adj state	0	<u>L46</u>
<u>L45</u>	IEEE1394 and active adj state and passive adj state	0	<u>L45</u>
<u>L44</u>	l40 and 1394	0	<u>L44</u>
<u>L43</u>	L40 and RS422	0	<u>L43</u>
<u>L42</u>	L40 and RS-422	0	<u>L42</u>
<u>L41</u>	L40 and usb	0	<u>L41</u>
<u>L40</u>	active adj state and passive adj state and imaging and camera	4	<u>L40</u>
<u>L39</u>	L38 and interface	3	<u>L39</u>
<u>L38</u>	L37 and USB	3	<u>L38</u>
<u>L37</u>	L36 and L33	6	<u>L37</u>
<u>L36</u>	imaging and active and passive and RS-232	60	<u>L36</u>
<u>L35</u>	maging and active and passive and RS-232	0	<u>L35</u>
<u>L34</u>	L33 and RS-232C	0	<u>L34</u>
<u>L33</u>	imaging and active and passive and RS-422	9	<u>L33</u>
<u>L32</u>	maging and active and passive and RS-422	0	<u>L32</u>
<u>L31</u>	imaging and active and passive and RS422	0	<u>L31</u>
<u>L30</u>	L28 and RS422	0	<u>L30</u>
<u>L29</u>	L28 and USB	0	<u>L29</u>
<u>L28</u>	L11 and imaging	6	<u>L28</u>